

Ref: KLEF/AR/Ph.D/Engineering/2025-26/1188

Date: 29-06-2026

NOTIFICATION

It is hereby notified that the Vice Chancellor, on the recommendations of the examiners appointed to adjudicate the Ph.D. thesis entitled **"DESIGN AND PERFORMANCE EVALUATION OF A FULL ADDER AND VEDIC-MULTIPLIER USING FINFET-BASED GDI METHODOLOGY"** submitted by **DILSHAD SHAIK, Research Scholar**, Koneru Lakshmaiah Education Foundation (Deemed to be University) with **Registered Number 173040007, Department of Electronics and Communication Engineering**, under the supervision of **Dr. G.Sai Krishna Santosh, Associate Professor, Department of ECE, KLEF** for the award of Ph.D. Degree, and on consideration of the report submitted by the Examiners of the VIVA-VOCE Board, has ordered that **DILSHAD SHAIK** be declared qualified for the award of the Degree of **Doctor of Philosophy (Ph.D)** in **"Electronics and Communication Engineering"** in the **"Faculty of Engineering"** of this University.



(Dr.K. Subba Rao)
Registrar

REGISTRAR
Koneru Lakshmaiah Education Foundation
(Deemed to be University)
Green Fields, VADDESWARAM-522 302.
Guntur District, Andhra Pradesh.

To

DILSHAD SHAIK,**Research Scholar,****Registered Number 173040007,****Department of Electronics and Communication Engineering,****Koneru Lakshmaiah Education Foundation (Deemed to be University).**